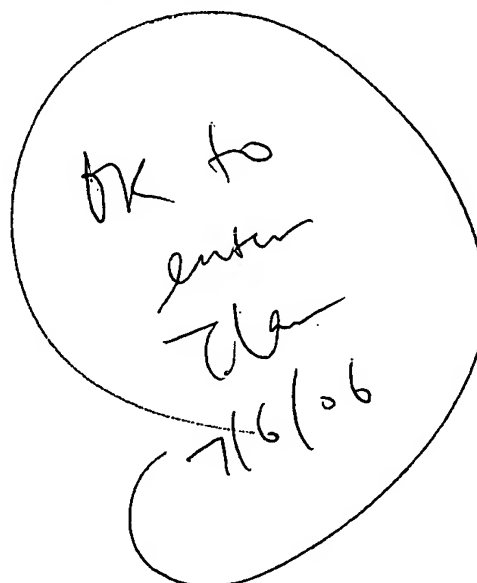


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**SWITCHED CAPACITOR CIRCUIT CAPABLE OF MINIMIZING CLOCK
FEEDTHROUGH EFFECT IN A VOLTAGE CONTROLLED OSCILLATOR
CIRCUIT**

Appl. No.	:	10/709,461	Confirmation No.	3460
Applicants	:	Chi-Ming Hsiao, Guang-Kai Dehng, Ming-Hong Tsai, Ling-Wei Ke, En-Hsiang Yeh, Chi-Kun Chiu		
Filed	:	May 6, 2004		
TC/A.U.	:	2816		
Examiner	:	LAM, TUAN THIEU		
Docket No.	:	MTKP0067USA1		
Customer No.	:	27765		



Commissioner for Patents
P.O. Box 1450
Alexandria VA 22313-1450

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INTRODUCTORY COMMENTS

The following is in response to the Office action of 03/29/2006 for the above-identified application:

Remarks begin on page 2 of this paper.

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